

ABSTRACT

A spectrum spread receiver is provided which correlatively executes in synchronism with a timing determined by each of multiple paths and conducts RAKE synthesis of correlating signal of each path, and which comprises: a plurality of finger processing circuits 100 (101, 102); a memory circuit 81, which stores inverse spread data of a unit length; a timing adjustment circuit 71, which outputs a timing signal at the time when a predetermined amount of inverse spread data are stored in a memory circuit; and a RAKE synthesizer 10 for conducting RAKE synthesis by reading a predetermined length of inverse spread data from the memory circuit 81 based on the timing signal output from the timing adjustment circuit 71.